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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	ATTORNEY DOCKET NO. CONFIRMATION NO.	
10/576,292	04/19/2006	Masato Taki	13596/4	4493	
23838 7590 10/07/2008 KENYON & KENYON LLP			EXAMINER		
1500 K STREET N.W. SUITE 700 WASHINGTON, DC 20005			WOJCIECHOWICZ, EDWARD JOSEPH		
			ART UNIT	PAPER NUMBER	
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			10/07/2008	PAPER	

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Application No. Applicant(s) 10/576,292 TAKLET AL. Office Action Summary Examiner Art Unit

		Edward Wojciechowicz	2895					
The MAILING DATE of this communication appears on the cover sheet with the correspondence address Period for Reply								
	• •							
WHIC - Exter after - If NO - Failu Any	ORTENED STATUTORY PERIOD FOR REPL. HEVER IS LONGER, FROM THE MALING DI nation of time may be available under the provision of 37 CFR 11. SOS (\$) INONTIFY from the mating date of the communication. period for riply is specified above, the maximum statutory period to reply is specified above, the maximum statutory period to reply the specified above, the maximum statutory period to reply the coffice later than three months after the mailing and patent term adjustment. See 37 CFR 1.704(p).	ATE OF THIS COMMUNICATION 36(a). In no event, however, may a reply be tin will apply and will expire SIX (6) MONTHS from cause the application to become ABANDONE	N. nely filed the mailing date of this o D (35 U.S.C. § 133).	,				
Status								
1)	Responsive to communication(s) filed on							
	☐ This action is FINAL. 2b) ☐ This action is non-final.							
3)□	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is							
	closed in accordance with the practice under Ex parte Quayle, 1935 C.D. 11, 453 O.G. 213.							
Dispositi	ion of Claims							
41121	Claim(s) 1-36 is/are pending in the application.							
	4a) Of the above claim(s) is/are withdrawn from consideration.							
	Claim(s) is/are allowed.							
6)🖂	☐ Claim(s) 1-36 is/are rejected.							
	Claim(s) is/are objected to.							
	Claim(s) are subject to restriction and/or	r election requirement.						
Applicati	ion Papers							
9)□	The specification is objected to by the Examine	r.						
. =	The drawing(s) filed on is/are: a) acce		Examiner.					
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).								
	Replacement drawing sheet(s) including the correct			FR 1.121(d).				
11)	The oath or declaration is objected to by the Ex	aminer. Note the attached Office	Action or form P	ГО-152.				
Priority (ınder 35 U.S.C. § 119							
12)	Acknowledgment is made of a claim for foreign	priority under 35 U.S.C. § 119(a))-(d) or (f).					
	All b) Some * c) None of:	. ,						
,	1. Certified copies of the priority documents	s have been received.						
	2. Certified copies of the priority documents	s have been received in Applicati	on No					
	3. Copies of the certified copies of the prior	rity documents have been receive	ed in this National	Stage				
	application from the International Bureau	ı (PCT Rule 17.2(a)).						
* 5	See the attached detailed Office action for a list	of the certified copies not receive	ed.					
Attachmen	t(s)							
	e of References Cited (PTO-892)	Interview Summary Paper No(s)/Mail Da						

3) X Information Disclosure Statement(s) (PTO/SE/08) 5) Notice of Informal Patent Application Paper No(s)/Mail Date 4-19-06. 6) Other: _____.

Application/Control Number: 10/576,292

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DETAILED ACTION

Claim Rejections - 35 USC § 112

Claims 1-36 are rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.

The exact structure of the inventive apparatus is not clearly defined. For example, in claim 15, the reference to "low potential" regions and "high potential" regions is vague as to just what differentiates these regions.

In addition, the specific structure of the "high withstand voltage separating region" is not clearly defined. How does the "high withstand voltage separating region" differ structurally from the "high potential" region?

How does the "high withstand voltage separating region" region differ structurally from the "insulating partition"? Do these regions serve the same purpose?

With respect of all of the regions mentioned above, what is the spatial relationship between these regions? That is, where is each located relative to the other?

What is the structure of the "relay semiconductor device"? Is this device an active device or a passive device?

If the "insulating partition" is arranged at least between the relay device and one of the "reference circuit regions" what structure does this partition have? Does it surround either of the regions?

The clause in claim 1 that describes the "insulating partition" contains wording that is unclear, i.e. "that is at the output one of the relay semiconductor device".

Similarly, the last clause of claim 1 does not read clearly, i.e. "... is wired to an output one of the low and high potential reference circuit regions". Also, it is unclear whether the reference circuit regions are wired or electrically coupled to the relay device?

To the extent that similar phrases to those in claim 1, are also recited in the dependent claims, these phrases are also indefinite.

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Any inquiry concerning this communication or earlier communications from the examiner should be directed to Edward Wojciechowicz whose telephone number is 571-272-1739. The examiner can normally be reached on Monday through Thursday.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Drew Richards can be reached on (571) 272-1736. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/Edward Wojciechowicz/ Primary Examiner, Art Unit 2895 Edward Wojciechowicz Primary Examiner Art Unit 2895

EW: ew